



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Aronson et al.

Serial No. 09/777,917

Filed: February 5, 2001

For: Integrated Memory Mapped Controller  
Circuit for Fiber Optics Transceiver

) Art Unit: 2633

) Examiner:  
unknown

) Attorney Docket:  
9775-0052-999

) September 18, 2002

#5/A  
h. MC Audlin  
10.1.02

PRELIMINARY AMENDMENT

**RECEIVED**

Assistant Commissioner for Patents  
Washington, D.C. 20231

SEP 20 2002

Technology Center 2600

Dear Sir:

This Amendment is submitted prior to receipt of a first office action.

IN THE CLAIMS:

Marked up versions of all revised claims, showing insertions and deletions, are included in Appendix A.

Rewrite the claims, and add new claims as follows:

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1 1. A single-chip integrated circuit for controlling an optoelectronic transceiver having a  
2 laser transmitter and a photodiode receiver, comprising:  
3 memory, including one or more memory arrays for storing information related to the  
4 transceiver;  
5 analog to digital conversion circuitry for receiving a plurality of analog signals from  
6 the laser transmitter and photodiode receiver, converting the received analog signals into  
7 digital values, and storing the digital values in predefined locations within the memory;  
8 control circuitry configured to generate control signals to control operation of the laser  
9 transmitter in accordance with one or more values stored in the memory;  
10 an interface for reading from and writing to locations within the memory; and

*Sub B1 A1 Cott.*